COMPUTER ENGINEERING DEPARTMENT

A. V. PAREKH TECHNICAL INSTITUTE, RAJKOT

Approved by AICTE & Affiliated to Gujarat Technological University
Department Of Technical Education (Government of Gujarat)
Opp. Hemutadhvi Hall, DrYagnik Road, RAJKOT
Ph.: 0281-2480175 Website:www.avpt.cteguj.inEmail:avpti-rajkot-dte@gujarat.gov.in

AVPT/COMP/PEDAGOGY/ 2024/025

January 31, 2025

Circular

A, pedagogy session on ***'Revisiting the Core: Advanced Concepts in Computer Number Systems'*** is organized on 01/02/2025 in Seminar Hall at 04:00 pm by Ms. Shivangi Malli, Lecturer, Computer Engineering Department, AVPTI- Rajkot. Therefore, it is to inform all staff members of computer engineering department to remain present during the pedagogy session on above mentioned topic.

Date and Time: 01/02/2025 at 04:00 pm

Venue : Seminar Hall

HOD, Computer engineering Department,
AVPTI, Rajkot

Copy Dispatched :

1) For the Computer engineering department office order file.
2) To the Computer engineering relevant staff for necessary action
4) For Computer engineering department Notice Board.

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सत्यमेव जयते

Date: 01-02-2025

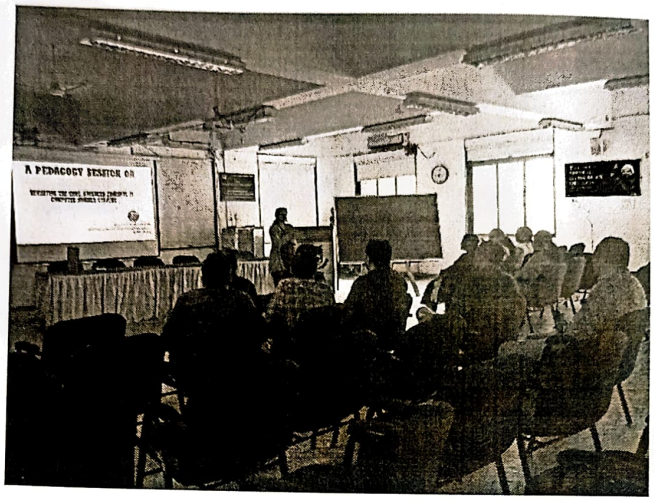
EVENT REPORT

DATE & TIME:	01-02-2025 at 4:00pm
EVENT TYPE:	Pedagogy Session
EVENT TITLE:	***Revisiting the Core: Advanced Concepts in Computer Number Systems***
EVENT VENUE:	Seminar Hall, Computer Engineering Department
EVENT CO-ORDINATOR:	S. B. MALLI (Lecturer CE)

NO. OF PARTICIPANTS :	**12**

EVENT OUTCOMES	• **Understanding of Advanced Number Systems**: Participants would gain an in-depth understanding of number systems beyond the basic binary, decimal, and hexadecimal systems. This may include topics like floating-point representation, octal, and other specialized systems used in computing. • **Applications of Number Systems in Computing:** The session could explore how these number systems are applied in areas like memory addressing, data storage, and digital signal processing, enhancing practical knowledge of their real-world significance. • **Conversion Techniques:** Learners would refine their skills in converting between various number systems (binary, hexadecimal, decimal, etc.) with a focus on more complex or real-world examples. • **Representation of Real Numbers:** Detailed exploration of how computers represent real numbers, covering topics like floating-point arithmetic, mantissa, exponent, and normalization, along with the limitations and implications of this representation in terms of precision and rounding errors. • **Binary Arithmetic:** Delving into the intricacies of binary arithmetic operations (addition, subtraction, multiplication, and division) in the context of digital circuits and algorithm design. • **Critical Thinking and Conceptualization:** Encouraging learners to think critically about how number systems influence both hardware (like processors) and software (such as compilers and interpreters), including an exploration of how these systems have evolved over time. In essence, the session would aim to provide participants with a stronger conceptual foundation in advanced number systems, as well as practical skills for applying this knowledge to real-world computing tasks.

EVENT HIGHLIGHTS



SIGNATURE

HEAD OF THE DEPARTMENT

A.V.PAREKH TECHNICAL INSTITUTE-RAJKOT

Computer Engineering Department

Date:

Pedagogy Session on

'Revisiting the Core: Advanced Concepts in Computer Number Systems'

Sr. No.	Faculty Name	Department	Signature
1	A.K. Panchasara	Comp. Engg.	
2	Y.A. Hatholiya	Comp Engg.	
3	S. V. Ramanuj	Comp. Eng.	
4	N R Radadiya	Comp. Dep	
5	Y J Pambhar	Comp. Eng	
6	R A Kadchha	Comp. Eng	
7	A-P Kurde	Comp. Eng.	
8	D S Pathak	Comp. Eng	
9	N K Shukla	Comp. Eng.	
10	H C Savaliya	Comp. Engg.	
11	M K Goswami	Comp. Eng	
12	L P Vyas	Comp. Engg.	
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